

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2750	indirect adj address\$8	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/05/24 14:09
L2	6	1 same (first adj register) same (second adj register)	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/05/24 14:15
L3	0	1 same (upper adj register) same (lower adj register)	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/05/24 14:15
L4	35	address\$6 same (upper adj register) same (lower adj register)	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/05/24 14:41
L5	10917	("32" with bus) same ("16" with bus)	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/05/24 14:41
L6	542	5 same counter	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/05/24 14:42
L7	61	5 same (address adj counter)	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/05/24 14:45
L8	22	5 same (address adj counter) same register\$4	US-PGPU B; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2007/05/24 14:42

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